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virtual mapping updating cache tags

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Write through virtual cache memory, alias addressing, and cache ...

Each cache tag comprises a truncated virtual address identifying the ... For updating the cache memory 16, the control flow of the cache control logic is ...

www.freepatentsonline.com/5933844.html - 44k - Cached - Similar pages

Virtual-Address Caches Part 1: Problems and Solutions in Uniprocessors

When different virtual addresses map to the same physical address, Even when synonyms are prevented, caches with virtual tags must be flushed at times ...

doi.ieeecomputersociety.org/10.1109/40.621215 - Similar pages

Hybrid cache having physical-cache and virtual-cache ...

For a virtual cache, the tag and index are derived from the virtual address of Because the first and second virtual addresses map to the same physical ...

www.patentstorm.us/patents/5479630-description.html - 48k - Cached - Similar pages

Method and apparatus for solving the stale data problem occurring ...

updating said destination virtual address with said new value on said cache miss and changing said data cache tag for a currently accessible virtual address ...

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V-P cache: A storage efficient virtual cache organization - Min ...

This secondary mapping function is applied only when the cache access based on ... from the virtual address, is a miss. The secondary mapping... (Update) ...

citeseer.ist.psu.edu/min93vp.html - 28k - Cached - Similar pages

[PPT] Lecture 15 Cache Associativity and Virtual Memory

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Since page tables contain a mapping for every virtual page, no tags are required. nightmare for update: must update all cache entries with same ...

www.1006.org/csa2006/slides/lesson15.ppt - Similar pages

[PPT] Fifth Lecture

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current write tag check & previous write cache update overlap; Pipeline affects only ... Two different virtual addresses map to the same physical address ...

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additionally has cache tags derived from the virtual ad- dress. While this type of design solved by the write—allocate cache update policy and by ...

ieeexplore.ieee.org/iel2/188/266/00004603.pdf?arnumber=4803 - Similar pages

[PPT] CPSC 614:Graduate Computer Architecture Memory System II

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Two different virtual addresses map to same physical address ... Pipeline Tag Check and Update Cache as separate stages; current write tag check & previous ...

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[PDF] TLB and Snoop Energy-Reduction using Virtual Caches in Low-Power ...

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The problem of physical tag-store lookups of virtual caches is that, virtual addresses might map differently to the cache than its corre- ...

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